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| **Teachers Name** | **Subject Name** | **Topic Name** | **Sub-topic Name** | **Type of E-Content** | **Course Name** | **Year of Course** | **Link** |
| **P C YADAV** | **Microprocessor** | 8086 Microprosessor | Architecture of 8086 Microprosessor | PDF | **B. Tech. CSE+EE**  **4TH SEM** | **2019-2020** | HALL. D. V (Book)  www.javapoint.com/8086-microprocessor |
|  |  | Memory Segmantation | Register Organization and Addressing Modes | PDF |  |  | HALL. D. V (Book)  www.javapoint.com/8086-microprocessor |
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|  |  | Assebly Language Programming based on intel 8085/8086 mp. | Stack and subroutines , conditional call and return instructions | PDF |  |  | HALL.D.V (Book)  R. S .Gaonkar |
|  |  | Peripheral Interfacing | 8255 PPI, 8255 operating Modes, 8237 DMA Controller, 8259 PIC, 8253/8254 Programmable timer / control | PDF |  |  | R .S. Goankar  https://youtu.be/mzItfJIEFMk |