**Mr. Vishal Yadav**

**Assistant Professor** 

Department of Electronics Engineering,

Uma Nath singh Institute of Engg and Technology

VBS Purvanchal University, Jaunpur, U.P.

**Life discipline** – Confident, Patience, Energetic, Persistent and Tenacious.

**Email Id**: vishalunsiet@gmail.com

**Qualification:**

B.Tech (Electronics and Instrumentation Engg.-UNSIET VBSPU JAUNPUR) – First Division (68%)in 2009.

M Tech (VLSI Design, SIET Jhalawa Allahabad(AKTU Lucknow)) – First Divison(72%) in 2014.

Ph D Pursuing from VBSPU Jaunpur.

**Teaching Experience-**

1. **Assistant Professor** in **“Uma Nath Singh Institute of Engineering and Technology”** , VBSPU, Jaunpur from 26 April 2010 to till date.

**Interests & Specialization**

1. VLSI Design
2. Transducer and Sensor
3. Electronics Engineering
4. NCER
5. EIM
6. Industrial Instrumentation
7. VLSI Technology
8. ICT

**Membership of Professional bodies**

**C**oordinator of Faculty Development Programs at Jaunpur, namely Recent advances in RF & Photonics.

**C**oordinator and organizer of various events like Science Day, Engineers Day, etc.

**C**oordinator of various students activities like Summer Internship for 2nd year students in 2019. **C**oordinator of Induction Program for 1st year students in 2019 and 2020.

**P**articipated in more than 20 in house and outside workshops and FDPs.

**A**ttended 15+ training, short term, FDPs programs in IITs, IISc, and Industries all over country.